TAU'19 WORKSHOP

MONTEREY, MARCH 21-22 2019

SONG CHEN (SYNOPSYS) – GENERAL CHAIR

JOÃO GEADA (ANSYS) – TECHNICAL PROGRAM CHAIR

WELCOME TO 26TH

ACM International Workshop on

Timing Issues in the Specification and Synthesis of Digital Systems

First of All

THANK YOU all for registering and coming to the workshop!

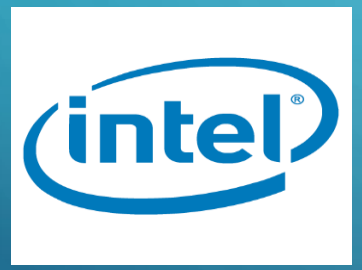
Your participation and contribution are critical to this event.

CALL FOR SUPPORT

- **Submit papers**
  - TAU is a workshop, both practical application and theoretical research work are welcome
  - You can still publish your paper at other conferences (DAC, ICCAD, DATE, ISPED, ISQED, etc.)
- **Volunteer talks**
  - Share your perspectives and experiences
  - Both solutions and questions
  - Pose challenges and controversial topics
- **Establish Network**
  - Connect with academia, EDA, industry: challenges, ideas, collaborations, etc.
  - Academia/industry: please submit ideas/challenges and participate TAU contest!

TAU 2019 SPONSORS

Thank you!



TAU 2019 PARTICIPATION

- ~35 attendees
- Academia, EDA, design houses, foundries
  - Democritus University of Thrace, Duke University, National Chiao Tung University, UC Berkeley, UC SD, UT Austin, Yale
  - Synopsys, Cadence, Ansys, Arcadia, Parallax
  - ARM, Broadcom, IBM, Inphy, Intel, MediaTek, nVidia, Qualcomm, ST.

TAU 2019 ORGANIZATION

Organization Committee

- General chair: Song Chen (Synopsys)
- TPC chair: João Geada (Ansys)
- Contest chair : George Chen (Intel)
- Past general chair: Tom Spyrou (Intel)

Contest Committee

- Jignesh Shah (Intel)
- Tsung-Wei Huang (UIUC)

Technical Program Committee

- Mayur Bubna (Synopsys)
- Prasanjeet Das (University of Southern California)
- Ravi Datla (Samsung Electonics)
- Chunyang Feng (Duke Kunshan University)
- Praveen Ghanta (Cadence)
- Masanori Hashimoto (Osaka University)
- Igor Keller (Cadence)
- Jiayong Le (Synopsys)
- Oleg Levitsky (Intel)
- Peng Li (Texas A&M University)
- Christian Lutkemeyer (inPhi)
- Oscar Ou (MediaTek)
- Paul Pereira (Qualcomm)
- Ken Stevens (University of Utah)
- K.S. Ramesh (Intel)
- Debjit Sinha (IBM)
- Tom Spyrou (Intel)
- Subramanyam Sripada (Synopsys)
- Ken Stevens (University of Utah)
- Peivand Tehrani (Synopsys)

TAU 2019 PROGRAM HIGHLIGHTS

- **Keynotes/Invited talks**
  - *Beyond CMOS – A Look at Two Promising Post CMOS Technologies: Superconducting Electronics & Spintronic* – Jamil kawa (Synopsys)
  - *Time as a First-Class Object in Systems* – Rick McGeer (engageLively)
  - *Where is my "Typical" Chip? Relating Silicon Back to the Timing Sign-Off Model* – Christian Lutkemeyer (Inphi)
  - *From Recovering Time to Timing Recovery: Some Challenges for the TAU Community* – Andrew B. Kahng (University of California, San Diego)
  - *Deep Neural Networks' Applications in EDA and Their Acceleration Techniques* – Yiran Chen (Duke University)
- **Panels**
  - *Post silicon reality vs. pre-silicon modeling – pitfalls and opportunities to expand the T-Workshop*
  - *Multiple Input Switching (MIS) effects in timing*
    - *Please ask your questions on TAU Website by tomorrow noon*
- **Contest**
  - *Design Optimization*

TAU 2019 LOGISTICS

- **Amenities**
  - Free WIFI: SSID: Hyatt-Meeting Password: Comp19
  - Free parking
- **Reception: Today**
  - Open bar: water, soft-drinks, beer, wine, etc. 6:30pm – 7:30pm
    - 2 tickets/person
  - Dinner 7:30pm – 9:00pm
- **Presenters**
  - Email slides to acmtauconference@outlook.com (directly to conference laptop)
  - Can use your own computer if needed
    - USB sticks